

# Nodal Analysis and Dependent Sources

The use of dependent sources when using Nodal Analysis.

## Nodal Analysis

Nodal analysis is a form of analysis that uses Kirchhoff's Current Law (KCL) and node equations to solve for circuit voltage values where the schematic diagram does not have any conductor paths crossing. A term typically used for this purpose is said to represent a planar circuit.

This is used to determine the voltage at each node( or connection point of two or more components) with respect to a reference node. The reference node is frequently called ground where the voltage at ground is equal to zero Volts.

In viewing schematic diagrams with either voltage sources or current sources, a reference node would usually be assigned to the negative terminal for a voltage source and to the opposite end when an arrow is shown for a current source. Another method of selecting the reference node would be to select the middle node when viewing all nodes.

There are two kinds of electrical sources: 1) independent and 2) dependent.

An independent source provides a fixed value of voltage or current to the connected circuit. Independent sources are power supplies and batteries. Power supplies provide a constant fixed value whereas batteries will not provide a constant fixed value over time without recharging them.

A dependent source is either a voltage source or current source whose value depends upon a voltage or current value somewhere else in the circuit. Dependent sources are useful tools in analyzing amplifiers. Two characteristics of amplifiers are voltage gain  $(A_V)$  and current gain (Ai). There are four basic linear dependent sources:

1. Voltage-controlled voltage source where the output is V, and  $A_V$  is the constant of proportionality (voltage gain), and  $V_{CD}$  is the parameter being sensed. The following equation is associated with a voltage-controlled voltage source:

#### V=AVVCDV=AVVCD

2. Current-controlled voltage source where the output is V, and  $R_M$  is the constant of proportionality (resistance), and  $I_C$  is the parameter being sensed. The following equation is associated with a current-controlled voltage source:

#### V=RMICV=RMIC

3. Current-controlled current source where the output is I, and Ai is the constant of proportionality (current gain), and  $I_C$  is the parameter being sensed. The following equation is associated with a current-controlled current source:

#### I=AIICI=AIIC

4. Voltage-controlled current source where the output is I, and GM is the constant of proportionality (conductance), and VCD is the parameter being sensed. The following equation is associated with a voltage-controlled current source:

#### I=(GM)(VCD)I=(GM)(VCD)

A nodal analysis with a dependent source occurs when there are two DC voltage sources and a DC current source as shown in Figure 1. Note the value for E1 is expressed in terms of an unknown value. E1 = 2Vx. Note that the voltage across resistor R1 is expressed as Vx. Note that the voltage across resistor R3 is expressed as V0. This information is to be used later in calculating node voltages.



Fig. 1

If the node or test point voltage were positive, it would read a positive value on the voltmeter. If the node or test point voltage were negative, it would read a negative value on the voltmeter.

An example problem for the circuit shown in Figure 1 would be to find the following:

A. The voltage across resistor R3 (V0).

B. The current through resistor R1 (IR1), R2 (IR2), R3 (IR3) and R4 (IR4).

C. Let I1 = 2 mAmps, I2 = 2 mAmps, E1 = 2Vx, E2 = 4 Volts, R1 = 1 Kilo Ohms, R2 = 2 kilo Ohms, R3 = 3 kilo Ohms, and R4 = 4 kilo Ohms.

D. Let the voltage across R1 (ER1) = Vx and E1 = V1 – V2.

The 1st step would be to identify the reference node or ground and then all the nodes of the circuit in Figure 1. Usually any independent source would be connected to ground and is shown in Figure 1.

In this circuit, there are nodes at the top of DC voltage source E1 labeled V1, below E1 labeled V2, below resistor R3 labeled V3, and above DC source E2 labeled V4. These nodes are shown in Figure 2.



The 2nd step would be to identify the super node, which has the dependent source with an unknown value of E1 that is equal to 2Vx. Draw a red line around V1 and V2 that has E1 within as shown in Figure 3.





The 3rd step would be to determine the independent source, which are the voltage source E2, and current sources I1 and I2. Draw a blue line around V4 and E2, I1 and I2 as shown in Figure 4.



The 4th step is to identify currents at the super node with a blue line and arrow labeled a, b, c, d and e as shown in Figure 5.



The 5th step is to identify currents at the V3 node with a red line and arrow labeled f, g and h as shown in Figure 6.



The next step will utilize Figure 5 that identifies the super node so Kirchhoff's Current Law (KCL) equations can be identified. Remember, Kirchhoff's Current Law (KCL) states the algebraic sum of all currents entering and exiting a node must equal zero.

The following KCL current equations can be written for the blue lines and arrows at the super node for a, b, c, d and e:

Note that:

The equation now becomes:

 $-I1+V2R1+V2-V3R2+V1-V3R3+V1-E2R4=0-I1+V2R1+V2-V3R2+V1-V3R3\\+V1-E2R4=0$ 

Substitute circuit values:

Multiply both sides by 12 k (Least Common Denominator):

Expand:

$$-24+12V2+6(V2-V3)+4(V1-V3)+3(V1-4)=0-24+12V2+6(V2-V3)+4(V1-V3)+3(V1-4)=0$$

Combine terms:

#### 7V1+18V2-10V3=367V1+18V2-10V3=36

[Equation 1]

The next step will utilize Figure 6 that shows the currents at the V3 node so the equations can be written for the red lines and arrows for f, g and h. Please note the directions of currents through resistor R2 and R3 are opposites when viewing the blue and red lines and arrows for c and g, and for d and h. This will be important later on in checking results.

-I2+V3-V2R2+V3-V1R3=0-I2+V3-V2R2+V3-V1R3=0

Substitute circuit values:

-(2 m)+V3-V22 k+V3-V13 k=0-(2 m)+V3-V22 k+V3-V13 k=0

Multiply both sides by 6 k (Least Common Denominator):

[-(2 m)+V3-V22 k+V3-V13 k=0](6 k)[-(2 m)+V3-V22 k+V3-V13 k=0](6 k)

Expand:

$$-12+3(V3-V2)+2(V3-V1)=0-12+3(V3-V2)+2(V3-V1)=0$$

-12+3V3-3V2+2V3-2V1=0-12+3V3-3V2+2V3-2V1=0

Combine terms:

[Equation 2]

There are two equations and three unknowns. Another equation is needed. When reviewing Figure 5 the given information concerning the DC voltage source E1 and nodes V2 and the voltage across resistor R1 that is Vx, another equation can be obtained.



[Equation A]

V2=VXV2=VX

[Equation B]

Using Equation B with V2 = Vx in Equation A, another equation for V1 can be obtained:

### V1-V2=2VXV1-V2=2VX V1-VX=2VXV1-VX=2VX

Solve for V1:

V1=2VX+VXV1=2VX+VX

V1=3VXV1=3VX

[Equation 3]

Now Equation 1 and Equation 2 need to have the V1 and V2 terms in terms of Vx and V3 by using Equation A and Equation B:

**Recall Equation 1:** 

7V1+18V2-10V3=367V1+18V2-10V3=36

7(3VX)+18(VX)-10V3=367(3VX)+18(VX)-10V3=36

21VX+18VX-10V3=3621VX+18VX-10V3=36

39VX-10V3=3639VX-10V3=36

[Equation C]

[Equation D] Now there are two equations and two unknowns that can be solved.

39VX-10V3=3639VX-10V3=36

[Equation C]

-9VX+5V3=12-9VX+5V3=12

[Equation D]

When 2 is multiplied on both sides of Equation D, the two equations could be added together causing the V3 terms to cancel leaving one equation with one unknown.

Multiply both sides of Equation D by 2:

(-9VX+5V3=12)(2)(-9VX+5V3=12)(2)

Expand:

-18VX+10V3=24-18VX+10V3=24

[Equation D]

Add the new Equation D to Equation C:

39VX-10V3=3639VX-10V3=36

[Equation C]

<u>-18VX+10V3=</u>24-18VX+10V3=24

[Equation D]

21VX=6021VX=60

Solve for Vx:

VX=2.86 v----VX=2.86 v\_

Recall:

V2=VXV2=VX

, Substitute

VX=2.86 voltsVX=2.86 volts

V2=2.86 v----V2=2.86 v\_

Recall:

#### V1=3V<mark>XV1=3V</mark>X

, Substitute

VX=2.86 volts VX=2.86 volts

V1=3(2.86 v)V1=3(2.86 v)

V1=8.58 v----V1=8.58 v\_

When Equation 1 and the calculated values for V1 and for V2 are used, V3 can be calculated:

Recall:

7V1+18V2-10V3=367V1+18V2-10V3=36

Substitute values for V1 and for V2:

7(8.58)+18(2.86)-10V3=367(8.58)+18(2.86)-10V3=36

Expand:

#### 60.06+51.48-10V3=3660.06+51.48-10V3=36

Combine terms:

111.54–10V3=36111.54–10V3=36

Solve for V3:

-10V3=-111.54+36-10V3=-111.54+36

Divide both sides by -10:

V3=7.55 v----V3=7.55 v\_

The value of V0 can be calculated by using Figure 5:

Recall:

V0=V1-V3V0=V1-V3

Substitute values for V1 and for V3:

V0=8.58 v-7.55 vV0=8.58 v-7.55 v

V0=1.03 v----V0=1.03 v\_

Now that all node voltages are known, currents can be calculated for resistor R1 (IR1), R2 (IR2), R3 (IR3) and R4 (IR4).

Recall:

IR1=V2R1IR1=V2R1

IR1=2.86 v1 k  $\Omega$ IR1=2.86 v1 k  $\Omega$ 

IR1=2.86 mA-----IR1=2.86 mA\_

Recall:

IR2=V2-V3R2IR2=V2-V3R2

IR2=2.86 v-7.55 v2 k ΩIR<mark>2=2.86 v</mark>-7.55 v2 k Ω

IR2=-4.69 v----IR2=-4.69 v\_

IR2=-2.35 mA-----IR2=-2.35 mA\_

Recall:

IR3=V1-V3R3IR3=V1-V3R3

IR3=8.58 v-7.55 v3 kΩIR3=8.58 v-7.55 v3 kΩ

#### IR3=1.03 v3 k $\Omega$ IR3=1.03 v3 k $\Omega$

IR3=0.34 mA----IR3=0.34 mA\_

#### Recall:

IR4=V1-V4R4IR4=V1-V4R4

IR4=8.58 v-4 v4 kΩIR4=8.58 v-4 v4 kΩ

IR4=4.58 v4 k $\Omega$ IR4=4.58 v4 k $\Omega$ 

IR4=1.15 mA-----IR4=1.15 mA\_

To confirm KCL current calculations, consider those associated at the super node:

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-I1+IR1+IR2+IR<mark>3+IR4=0</mark>-I1+IR1+IR2+IR3+IR4=0
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Substitute circuit values:

(-2 mA)+2.86 mA-2.35 mA+0.34 mA+1.15 mA=0(-2 mA)+2.86 mA-2.35 mA+ 0.34 mA+1.15 mA=0

Combine terms:

4.35 mA-4.35 mA=04.35 mA-4.35 mA=0

To confirm KCL current calculations at the V3 node:

-I2+IR2+IR3=0-I2+IR2+IR3=0

Note IR2 and IR3 are opposites signs from the super node calculations

(-2 mA)+2.35 mA-0.34 mA=0(-2 mA)+2.35 mA-0.34 mA=0

Combine terms:

A voltage controlled current source is where the output current (IS) is a linear function of a connected component that has a referenced voltage (VX) in the following relationship:

IS = (A) (IX) Where A is a multiplier and IX needs to be determined.

The following diagram of a VCCS is illustrated in Figure 1.



Figure 1. Voltage Controlled Current Source

Consider the following circuit that consists of a dependent voltage source I2 having a value of (-2mA) (VR1), a connection link to a resistor R1 that has a value 1K Ohms with a voltage drop of VR1, an independent voltage source V1 having a value of 4 Volts and an independent current source I1 having a value of 1 mAmp as illustrated in Figure 2.



Before using Kirchhoff's Current Law (KCL) at node B, the voltage at node A can be determined using the value of the independent voltage source V1 of 4 Volts:

VA=V1=4 voltsVA=V1=4 volts

To determine the KCL at node A, currents need to be identified. Current IA is positive because it enters the node whereas current IB and IC are negative because they leave the node as illustrated in Figure 3.



Substitute circuit values:

#### IA–VB–41 kΩ–1 mA=0IA–VB–41 kΩ–1 mA=0

Multiply both sides by 1k:

Expand:

$$(1 \text{ k})IA-VB+4-1=0(1 \text{ k})IA-VB+4-1=0$$

Combine terms:

Unknowns on left, knowns on right

$$(1 \text{ k})IA-VB=-3(1 \text{ k})IA-VB=-3$$

[Equation 1]

To determine the KCL at node B, currents need to be identified. Current IA, IB, and IC are positive because they enter the node whereas current IE is negative because it leaves the node as illustrated in Figure 4.



The algebraic sum of all currents at node VB are equal to zero:

#### IB+IC+ID-IE=0IB+IC+ID-IE=0

Note that:

#### IB=VA-VBR1IB=VA-VBR1

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IC=I1IC=I1
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#### ID=I2ID=I2

$$I_{E} = \{V_{B}\} \{R_{2}\}$$

So that:

Substitute circuit values:

$$4-VB1 \text{ k}\Omega+1 \text{ mA}-(2 \text{ m})\text{VR}1-\text{VB2 k}\Omega=04-\text{VB1 k}\Omega+1 \text{ mA}-(2 \text{ m})\text{VR}1-\text{VB2 k}\Omega=0$$

Multiply both sides by 2 k:

 $\frac{[4-VB1 k\Omega+1 mA-(2 m)VR1-VB2 k\Omega=0](2 k)[4-VB1 k\Omega+1 mA-(2 m)VR1-VB2 k\Omega=0](2 k)}{B2 k\Omega=0](2 k)}$ 

Expand:

$$2(4-VB)+2-4VR1-VB=02(4-VB)+2-4VR1-VB=0$$

8-2VB+2-4VR1-VB=08-2VB+2-4VR1-VB=0

Combine terms:

-3VB-4VR1+10=0-3VB-4VR1+10=0

Unknowns on left, knowns on right

[Equation 2]

Use Ohm's Law to find ER4:

ER4=I1R4ER4=I1R4

Substitute circuit values:

 $ER4=(1 \text{ mA})(3 \text{ k}\Omega)ER4=(1 \text{ mA})(3 \text{ k}\Omega)$ 

ER4=3 v----ER4=3 v\_

Note that R4 is parallel to R1. That makes ER4 equal to VR1.

VR1=3 v-----VR1=3 v\_

Use Ohms Law to find IB:

IB=VR1R1IB=VR1R1

Substitute circuit values:

IB=3 v1 kΩIB=3 v1 kΩ

IB=3 mA----IB=3 mA\_

Recall currents at node A:

IA-IB-IC=0IA-IB-IC=0

Solve for IA:

IA=IB+ICIA=IB+IC

Substitute circuit values:

IA=(3 mA)+(1 mA)IA=(3 mA)+(1 mA)

IA=4 mA---IA=4 mA\_

Note that current ID is current I2:

ID=(-2 m)(VR1)ID=(-2 m)(VR1)

Substitute circuit values:

$$ID=(-2 m)(3 v)ID=(-2 m)(3 v)$$
  
 $ID=-6 mA----ID=-6 mA$ 

Recall currents at node B:

IB+IC+ID-IE=0IB+IC+ID-IE=0

Solve for IE:

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IE=IB+IC+IDIE=IB+IC+ID
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Substitute circuit values:

IE=(3 mA)+(1 mA)+(-6 mA)IE=(3 mA)+(1 mA)+(-6 mA)

IE=-2 mA-----IE=-2 mA\_

Note that VR1 can be determined from node A and node B:

VR1=VA-VBVR1=VA-VB

Solve for VB:

Substitute circuit values:

$$VB = (4 v) - (3 v)VB = (4 v) - (3 v)$$
$$VB = 1 v - - - VB = 1 v_{-}$$

Use the equation associated with the dependent current source to find current I2:

I2=(-2 m)(VR1)I2=(-2 m)(VR1)

Substitute circuit values:

$$I2=(-2 m)(3 v)I2=(-2 m)(3 v)$$
  
 $I2=-6 mA----I2=-6 mA$ 

Use Ohms Law to find the voltage across resistor R3:

ER3=(I2)(R3)ER3=(I2)(R3)

Substitute circuit values:

$$ER3=(-6 \text{ mA})(4 \text{ k}\Omega)ER3=(-6 \text{ mA})(4 \text{ k}\Omega)$$

ER3=-24 v----ER3=-24 v\_

To confirm KVL loop involving the independent voltage source V1 and resistors R1 and R2 the following equation is obtained:

Substitute circuit values and note ER2 equals VB

(4 v)=(3 v)+(1 v)(4 v)=(3 v)+(1 v)

Nodal analysis with dependent sources utilized Kirchhoff's Current Law with Algebra and Ohm's Law to substitute an unknown voltage for a node and to find other circuit values. By taking the time to carefully label the nodes, by identifying the proper node voltages and polarities, problem solving is made easier and can avoid mistakes.